

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IPE 2826

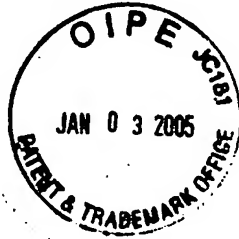
Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US4

Filed: October 18, 2000

Examiner: Johannes P Mondt



Serial No.: 09/691,004

Due Date: December 30, 2004

Group Art Unit: 2826

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450


Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

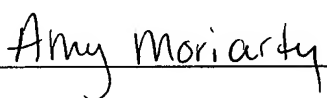
- ☒ A return postcard.
- ☒ An Amendment and Response (10 Pages).
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 3 cited documents.

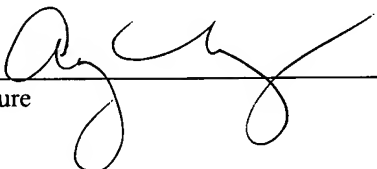
If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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By: 
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 30th day of December, 2004.


Name


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(GENERAL)